

Avinashi Road, Arasur, Coimbatore.

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NBA Accredited (CSE, ECE, EEE, MECH, CIVIL)

INDUSTRY EXPERT TALK "OPPORTUNITIES IN VLSI INDUSTRIES"
EC001

Event No	EC001
Organizing Department	Electronics and Communication Engineering
Date	09/02/2024
Time	10:00 AM to 11:00 AM
Event Type	Expert Talk
Event Level	Dept. Level
Venue	D-106
Total Participants	60
Students - Internal	60

Related SDG



Resource Persons

SI	Туре	Name	Designation	Company	Email	Phone
1	Chief Guest	Mr V Navaneethak rishnan	Sr. Field Application Engineer,	Entuple Technologies, Bengaluru	navaneethakrishnan.v@entuple.com	xxxxxxxxx

Involved Staffs

SI	Name	Role
1	Kathirvelu M	Convenor
2	Muralidharan J	Co-convenor
3	Prasad J	Co-convenor

Outcome

At the end of the session, students were able to understandASIC design flow. Applications of VLSI in different domains. The role of the VLSI engineer in Application Engineer Verification Engineer Chip design Engineer Design Engineer Physical Design Engineer DFT Engineer

Event Summary

The department of Electronics and Communication Engineering students' association SPARTRANZ organizes an expert talk on "Opportunities in VLSI Industries" on February 9, 2024, in D-106. Mr. V. Navaneethakrishnan, Sr. Field Applications Engineer from Entuple Technologies Banglore, was the industry expert. This expert talk is organized for third-year ECE students to enhance their knowledge in the VLSI domain, in which they are studying the VLSI design course in the current semester. The session started with a welcome to the gathering and an introduction about the industry expert, then the session handed over to the expert. The expert starts his presentation with an overview of Entuple Technologies and their service to society. The following topics are covered during his presentation: ASIC design flow and its types. Evaluation of VLSI Technologies. Applications of VLSI and ASIC inMedical/HealthcareDefenseConsumer ElectronicsAutomotive industryTelecommunicationCareer opportunities in VLSI industries. Front-end designBack-end design Future scope of VLSI technologies. After his presentation was over, the students actively interacted with the expert through a question-and-answer session. The faculty member in charge of the event, Dr.J. Prasad, AP (SI.G.)/ECE, read the vote of thanks.



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